

DECLARATION OF SOLE INVENTOR FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: Methods of Forming Oxide Regions Over Semiconductor Substrates, and Methods of Forming Transistors Associated With Semiconductor Substrates, the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations §1.56.

PRIOR FOREIGN APPLICATIONS:

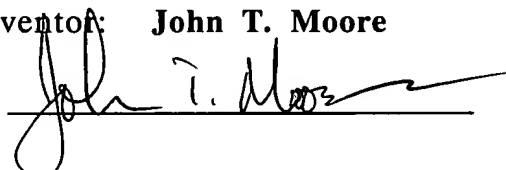
I hereby state that no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were

1 made with the knowledge that willful false statements and the like so
2 made are punishable by fine or imprisonment, or both, under
3 Section 1001 of Title 18 of the United States Code and that such willful
4 false statement may jeopardize the validity of the application or any
5 patent issued therefrom.

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9 Full name of sole inventor: **John T. Moore**

10 Inventor's Signature: 

11 Date: 6/14/00

12 Residence: **Boise, ID**

13 Citizenship: **US**

14 Post Office Address: **12530 W. Lexus Ct.**
Boise, ID 83713

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. Filed herewith
 Filing Date Filed herewith
 Inventor John T. Moore
 Assignee Micron Technology, Inc
 Group Art Unit Unknown
 Examiner Unknown
 Attorney's Docket No. MI22-1384
 Title: Methods of Forming Oxide Regions Over Semiconductor Substrates, and
 Methods of Forming Transistors Associated With Semiconductor Substrates

**POWER OF ATTORNEY BY ASSIGNEE AND
 CERTIFICATE BY ASSIGNEE UNDER 37 CFR §3.73(b)**

To: Assistant Commissioner for Patents
 Washington, D.C. 20231

Sir:

MICRON TECHNOLOGY, INC., the Assignee of the entire right,
 title and interest in the above-identified patent application by assignment
 attached hereto, hereby appoints the attorneys and agents of the firm of
 WELLS, ST. JOHN, ROBERTS, GREGORY & MATKIN P.S., listed as
 follows:

David P. Roberts	Reg. No. 23,032
Randy A. Gregory	Reg. No. 30,386
Mark S. Matkin	Reg. No. 32,268
James L. Price	Reg. No. 27,376
Deepak Malhotra	Reg. No. 33,560
Mark W. Hendricksen	Reg. No. 32,356
David G. Latwesen	Reg. No. 38,533
George G. Grigel	Reg. No. 31,166
Keith D. Grzelak	Reg. No. 37,144
James D. Shaurette	Reg. No. 39,833
Frederick M. Fliegel	Reg. No. 36,138
Donald Brent Kenady	Reg. No. 40,045

James E. Lake
Bernard Berman

Reg. No. 44,854
Reg. No. 37,279

and also attorneys Michael L. Lynch (Reg. No. 30,871) and Lia Pappas
Dennison (Reg. No. 34,095) of Micron Technology, Inc., as its attorneys
with full power of substitution to prosecute this application and transact
all business in the Patent and Trademark Office connected therewith.

The Assignee certifies that the above-identified Assignment has
been reviewed and to the best of Assignee's knowledge and belief, title
is in the Assignee, and a copy of the Assignment is submitted herewith.

Please direct all correspondence regarding this application to:

Customer No. 021567
Wells, St. John, Roberts, Gregory & Matkin P.S.
Attn: David G. Latwesen, Ph.D.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Telephone: (509) 624-4276
Facsimile: (509) 838-3424

MICRON TECHNOLOGY, INC.

Dated: 6-16-90

By: 

Name: Michael L. Lynch, Esq.
Title: Chief Patent Counsel

Attachment: Copy of Assignment; Copy of Board of Directors' Resolution

[illegible]

NOW THEREFORE BE IT RESOLVED, that the Board hereby approves and authorizes Mr. Michael L. Lynch, Assistant General Counsel for Intellectual Property, to execute on behalf of the Company, documents pertaining to the Company's patent prosecution matters, including but not limited to documents relating to representation before a patent examining authority, patent terms and other patent prosecution procedures, both in the United States and other countries, upon such terms and conditions as the General Counsel of the Company shall deem necessary or appropriate.

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
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The resolutions attached hereto are in conformity with the Articles of Incorporation and Bylaws of the Corporation and are now in full force and effect.

NAME AND TITLE

SIGNATURE

**Michael L. Lynch, Assistant General
Counsel for Intellectual Property**

NT 7.3

IN WITNESS WHEREOF, I have hereunto subscribed my name and affixed the seal of the said corporation, this 10th day of May, 1996.

Jan R. Reimer
Jan R. Reimer, Assistant Secretary

(SEAL)